

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Othmar Leitner, et al.	Art Unit	: 2812
Serial No.	: 10/579,487	Examiner	: Unknown
Filed	: May 16, 2006	Conf. No.	: 3015
Title	: METHOD FOR THE PRODUCTION OF TRANSISTOR STRUCTURES WITH LDD		

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Please consider the reference listed on the enclosed PTO-1449 form. A foreign patent document is enclosed.

This statement is being filed after a first Office action on the merits, but before receipt of a final Office action or a Notice of Allowance. The late submission fee of §1.17(p) in the amount of \$180 is being paid electronically with this submission. Apply any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

December 31, 2008  
Date: \_\_\_\_\_

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